

# FGH50T65UPD

## 650 V, 50 A Field Stop Trench IGBT

### Features

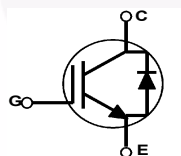
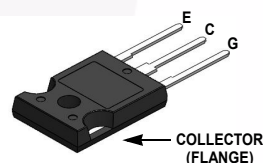
- Maximum Junction Temperature :  $T_J = 175^{\circ}\text{C}$
- Positive Temperature Co-efficient for Easy Parallel Operating
- High Current Capability
- Low Saturation Voltage:  $V_{CE(sat)} = 1.65\text{ V(Typ.) @ } I_C = 50\text{ A}$
- 100% of Parts Tested  $I_{LM(2)}$
- High Input Impedance
- Tightened Parameter Distribution
- RoHS Compliant
- Short Circuit Ruggedness > 5 us @  $25^{\circ}\text{C}$

### General Description

Using innovative field stop trench IGBT technology, Fairchild's new series of field-stop trench IGBTs offer optimum performance for solar inverter, UPS, welder, and digital power generator where low conduction and switching losses are essential.

### Applications

- Solar Inverter, UPS, Welder, Digital Power Generator
- Telecom, ESS



### Absolute Maximum Ratings

Symbol	Description	Ratings	Unit
$V_{CES}$	Collector to Emitter Voltage	650	V
$V_{GES}$	Gate to Emitter Voltage	$\pm 20$	V
	Transient Gate to Emitter Voltage	$\pm 25$	V
$I_C$	Collector Current @ $T_C = 25^{\circ}\text{C}$	100	A
	Collector Current @ $T_C = 100^{\circ}\text{C}$	50	A
$I_{CM(1)}$	Pulsed Collector Current	150	A
$I_{LM(2)}$	Clamped Inductive Load Current @ $T_C = 25^{\circ}\text{C}$	150	A
$I_F$	Diode Forward Current @ $T_C = 25^{\circ}\text{C}$	60	A
	Diode Forward Current @ $T_C = 100^{\circ}\text{C}$	30	A
$I_{FM(1)}$	Pulsed Diode Maximum Forward Current	150	A
$P_D$	Maximum Power Dissipation @ $T_C = 25^{\circ}\text{C}$	340	W
	Maximum Power Dissipation @ $T_C = 100^{\circ}\text{C}$	170	W
SCWT	Short Circuit Withstand Time @ $T_C = 25^{\circ}\text{C}$	5	us
$T_J$	Operating Junction Temperature	-55 to +175	$^{\circ}\text{C}$
$T_{stg}$	Storage Temperature Range	-55 to +175	$^{\circ}\text{C}$
$T_L$	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds	300	$^{\circ}\text{C}$

#### Notes:

1: Repetitive rating: Pulse width limited by max. junction temperature

2:  $I_C = 150\text{ A}$ ,  $V_{CE} = 400\text{ V}$ ,  $R_g = 10\ \Omega$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JC}(\text{IGBT})$	Thermal Resistance, Junction to Case	-	0.44	$^{\circ}\text{C/W}$
$R_{\theta JC}(\text{Diode})$	Thermal Resistance, Junction to Case	-	1.2	$^{\circ}\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	-	40	$^{\circ}\text{C/W}$

## Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FGH50T65UPD	FGH50T65UPD	TO-247 A03	Tube	N/A	N/A	30

## Electrical Characteristics of the IGBT T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
BV <sub>CES</sub>	Collector to Emitter Breakdown Voltage	V <sub>GE</sub> = 0 V, I <sub>C</sub> = 1 mA	650	-	-	V
$\frac{\Delta BV_{CES}}{\Delta T_J}$	Temperature Coefficient of Breakdown Voltage	V <sub>GE</sub> = 0 V, I <sub>C</sub> = 250 uA	-	0.65	-	V/°C
I <sub>CES</sub>	Collector Cut-Off Current	V <sub>CE</sub> = V <sub>CES</sub> , V <sub>GE</sub> = 0 V	-	-	250	μA
I <sub>GES</sub>	G-E Leakage Current	V <sub>GE</sub> = V <sub>GES</sub> , V <sub>CE</sub> = 0 V	-	-	±400	nA
On Characteristics						
V <sub>GE(th)</sub>	G-E Threshold Voltage	I <sub>C</sub> = 50 mA, V <sub>CE</sub> = V <sub>GE</sub>	4.0	6.0	7.5	V
V <sub>CE(sat)</sub>	Collector to Emitter Saturation Voltage	I <sub>C</sub> = 50 A, V <sub>GE</sub> = 15 V	-	1.65	2.3	V
		I <sub>C</sub> = 50 A, V <sub>GE</sub> = 15 V, T <sub>C</sub> = 175°C	-	2.1	-	V
Dynamic Characteristics						
C <sub>ies</sub>	Input Capacitance	V <sub>CE</sub> = 30 V, V <sub>GE</sub> = 0 V, f = 1 MHz	-	3540	4710	pF
C <sub>oes</sub>	Output Capacitance		-	110	146	pF
C <sub>res</sub>	Reverse Transfer Capacitance		-	60	90	pF
Switching Characteristics						
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>CC</sub> = 400 V, I <sub>C</sub> = 50 A, R <sub>G</sub> = 6.0 Ω, V <sub>GE</sub> = 15 V, Inductive Load, T <sub>C</sub> = 25°C	-	32	41	ns
t <sub>r</sub>	Rise Time		-	59	77	ns
t <sub>d(off)</sub>	Turn-Off Delay Time		-	160	208	ns
t <sub>f</sub>	Fall Time		-	22	29	ns
E <sub>on</sub>	Turn-On Switching Loss		-	2.7	3.5	mJ
E <sub>off</sub>	Turn-Off Switching Loss		-	0.74	0.96	mJ
E <sub>ts</sub>	Total Switching Loss	V <sub>CC</sub> = 400 V, I <sub>C</sub> = 50 A, R <sub>G</sub> = 6.0 Ω, V <sub>GE</sub> = 15 V, Inductive Load, T <sub>C</sub> = 175°C	-	3.44	4.46	mJ
t <sub>d(on)</sub>	Turn-On Delay Time		-	29	-	ns
t <sub>r</sub>	Rise Time		-	72	-	ns
t <sub>d(off)</sub>	Turn-Off Delay Time		-	166	-	ns
t <sub>f</sub>	Fall Time		-	19	-	ns
E <sub>on</sub>	Turn-On Switching Loss		-	3.5	-	mJ
E <sub>off</sub>	Turn-Off Switching Loss		-	1.2	-	mJ
E <sub>ts</sub>	Total Switching Loss		-	4.7	-	mJ
T <sub>SC</sub>	Short Circuit Withstand Time	V <sub>GE</sub> = 15 V, V <sub>CC</sub> =400 V, R <sub>G</sub> = 10 Ω	5	-	-	us
Q <sub>g</sub>	Total Gate Charge	V <sub>CE</sub> = 400 V, I <sub>C</sub> = 50 A, V <sub>GE</sub> = 15 V	-	230	345	nC
Q <sub>ge</sub>	Gate to Emitter Charge		-	31	47	nC
Q <sub>gc</sub>	Gate to Collector Charge		-	130	195	nC

**Electrical Characteristics of the Diode**  $T_C = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Test Conditions		Min.	Typ.	Max	Unit
V <sub>FM</sub>	Diode Forward Voltage	I <sub>F</sub> = 30 A	T <sub>C</sub> = 25°C	-	2.1	2.7	V
			T <sub>C</sub> = 175°C	-	1.78	-	
E <sub>rec</sub>	Reverse Recovery Energy	I <sub>F</sub> = 30 A, di <sub>F</sub> /dt = 200 A/μs	T <sub>C</sub> = 175°C	-	46	-	uJ
t <sub>rr</sub>	Diode Reverse Recovery Time		T <sub>C</sub> = 25°C	-	41	53	ns
			T <sub>C</sub> = 175°C	-	144	-	
Q <sub>rr</sub>	Diode Reverse Recovery Charge		T <sub>C</sub> = 25°C	-	76	106	nC
			T <sub>C</sub> = 175°C	-	486	-	

## Typical Performance Characteristics

Figure 1. Typical Output Characteristics

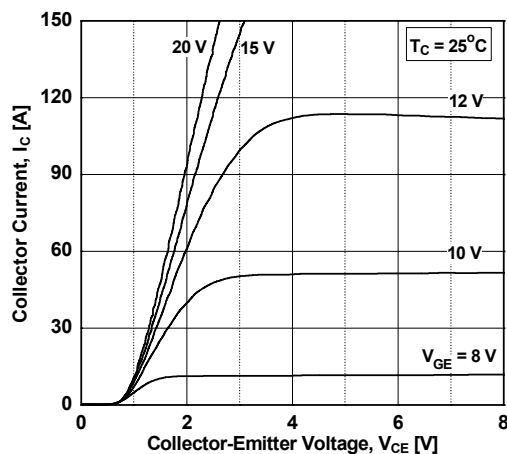


Figure 2. Typical Output Characteristics

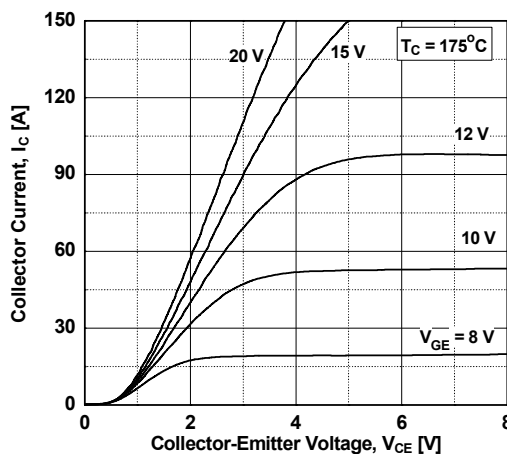


Figure 3. Typical Saturation Voltage Characteristics

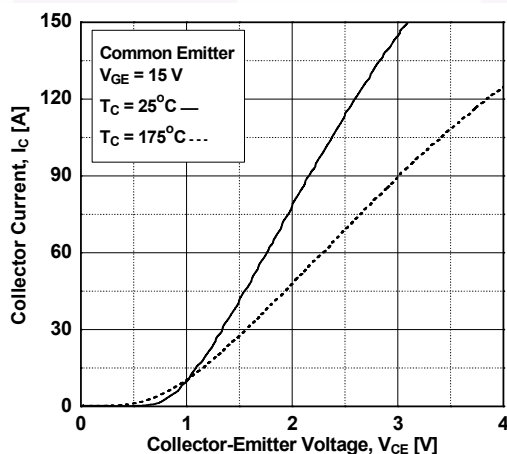


Figure 4. Saturation Voltage vs. Case Temperature at Variant Current Level

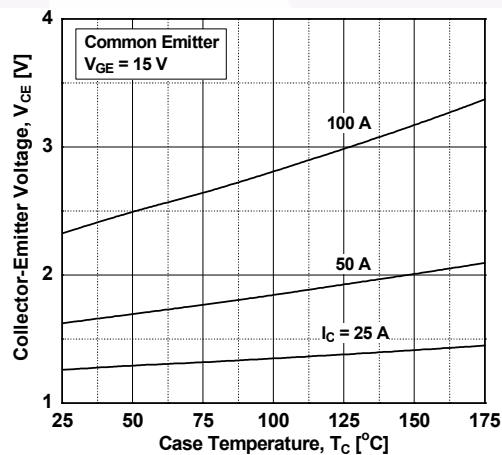


Figure 5. Saturation Voltage vs.  $V_{GE}$

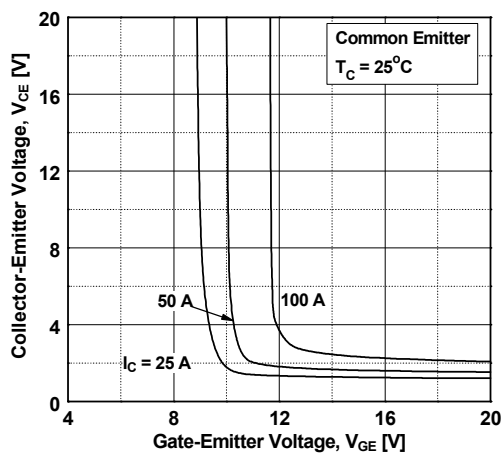
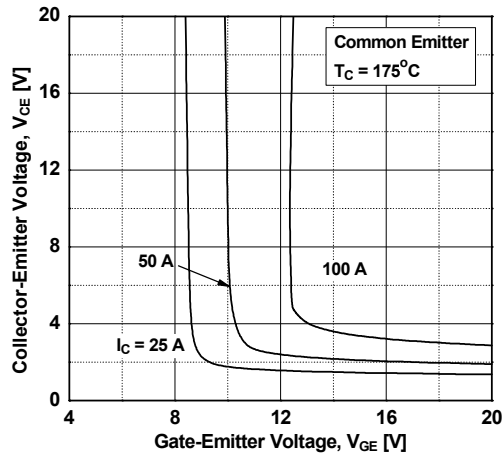


Figure 6. Saturation Voltage vs.  $V_{GE}$



## Typical Performance Characteristics

Figure 7. Capacitance Characteristics

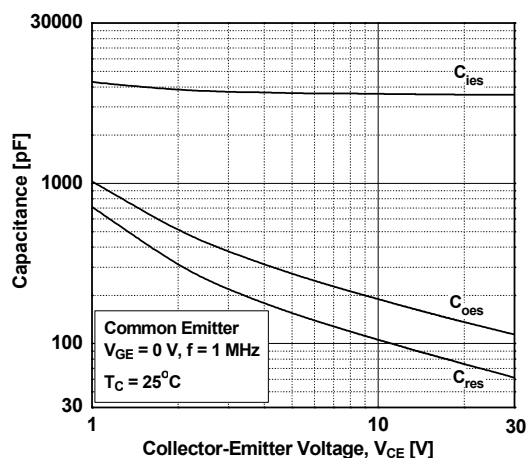


Figure 8. Gate charge Characteristics

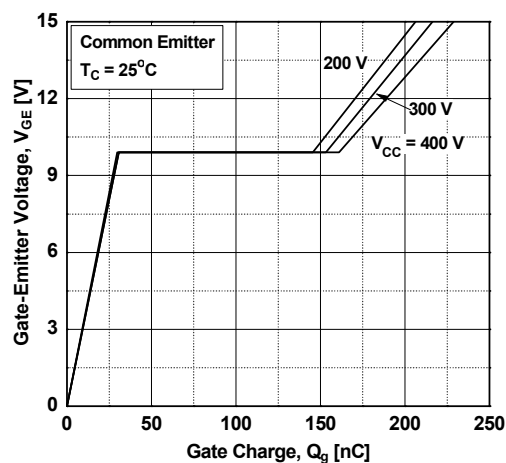


Figure 9. Turn-on Characteristics vs. Gate Resistance

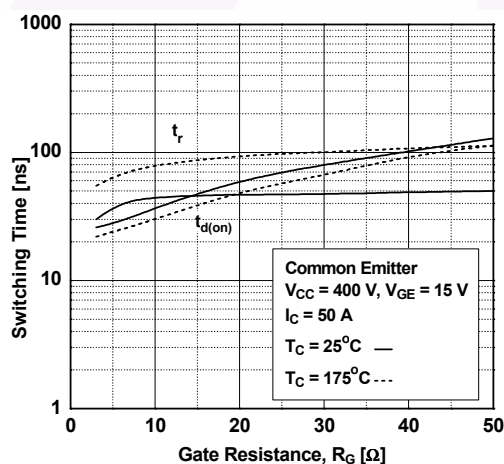


Figure 10. Turn-off Characteristics vs. Gate Resistance

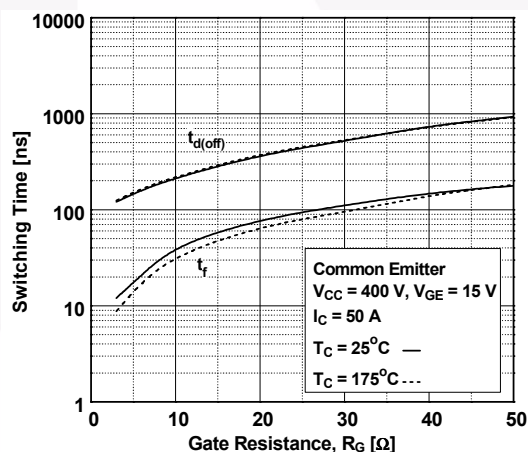


Figure 11. Switching Loss vs. Gate Resistance

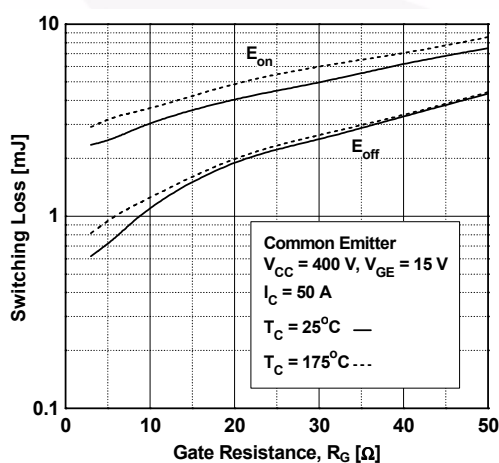
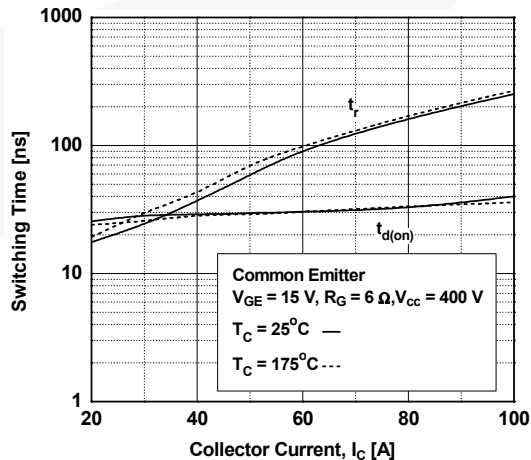


Figure 12. Turn-on Characteristics vs. Collector Current



## Typical Performance Characteristics

Figure 13. Turn-off Characteristics vs. Collector Current

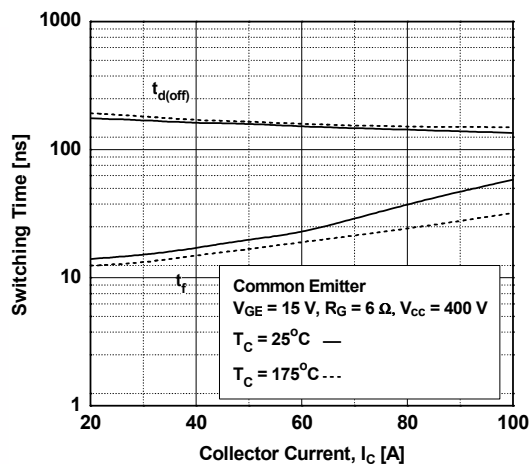


Figure 14. Switching Loss vs. Collector Current

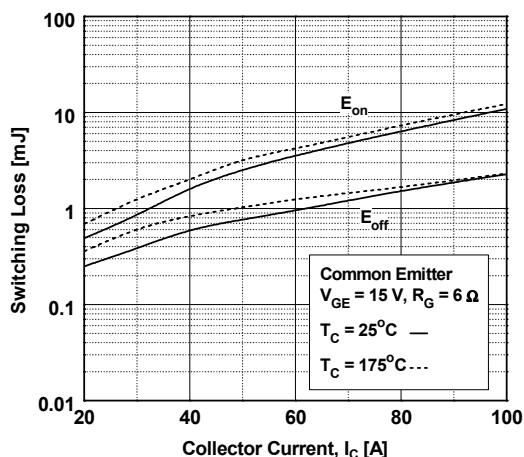


Figure 15. Load Current vs. Frequency

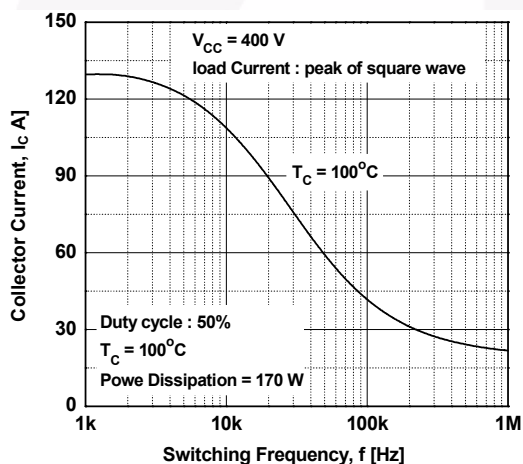


Figure 16. SOA Characteristics

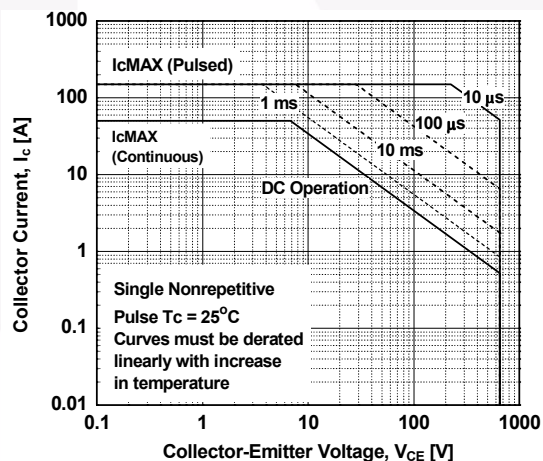


Figure 17. Forward Characteristics

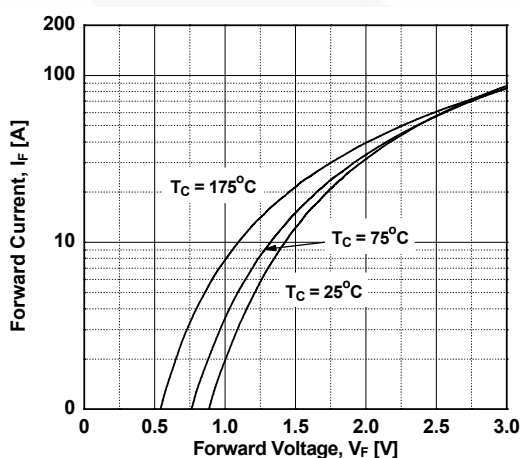
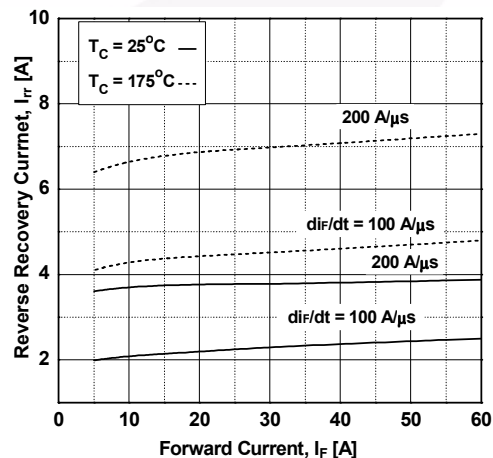


Figure 18. Reverse Recovery Current



## Typical Performance Characteristics

Figure 19. Reverse Recovery Time

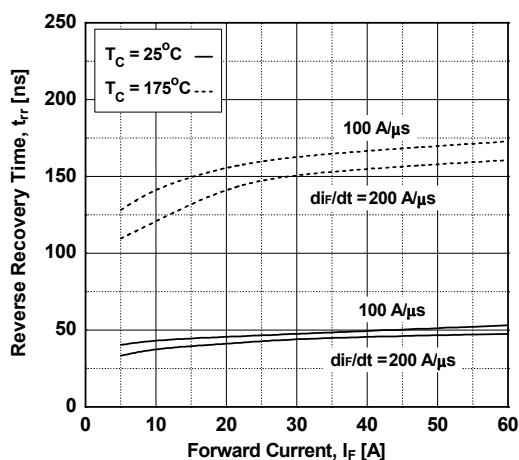


Figure 20. Stored Charge

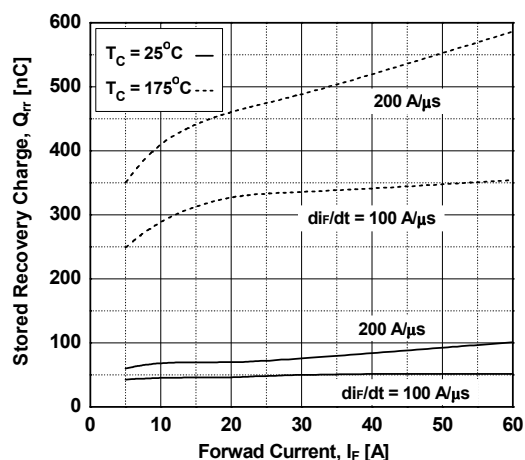


Figure 21. Transient Thermal Impedance of IGBT

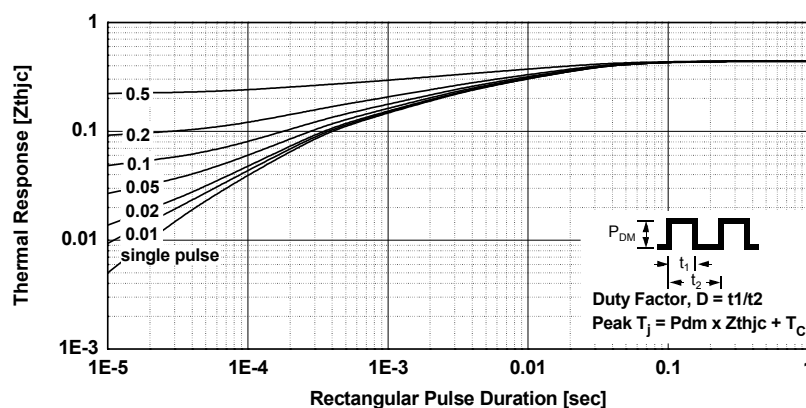
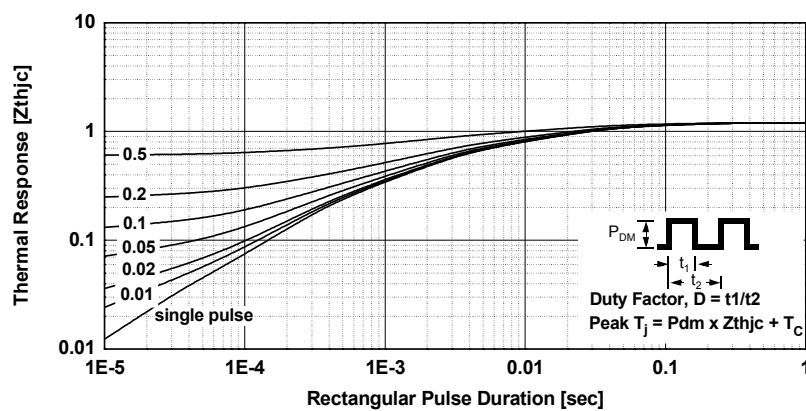
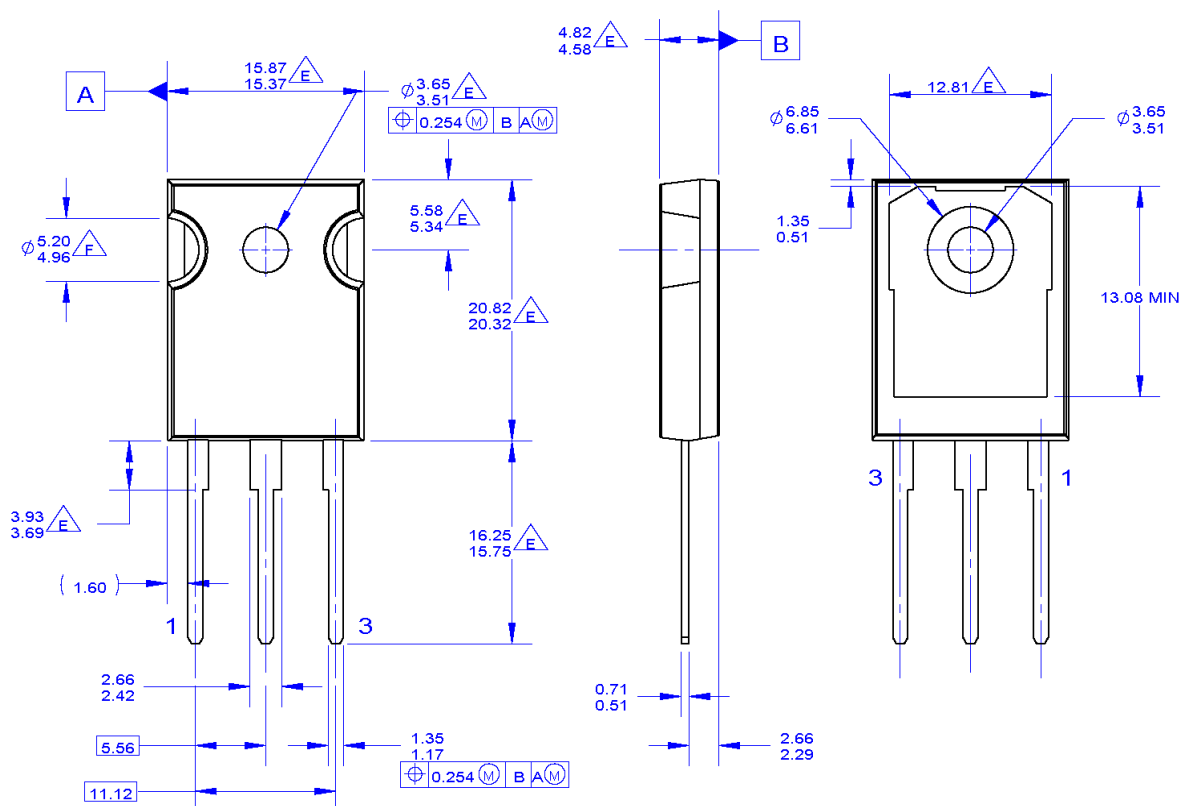


Figure 22. Transient Thermal Impedance of Diode



## Mechanical Dimensions



NOTES: UNLESS OTHERWISE SPECIFIED.

- A. PACKAGE REFERENCE: JEDEC TO-247, ISSUE E, VARIATION AB, DATED JUNE, 2004.
- B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- C. ALL DIMENSIONS ARE IN MILLIMETERS.
- D. DRAWING CONFORMS TO ASME Y14.5 - 1994

$\triangle E$  DOES NOT COMPLY JEDEC STANDARD VALUE

$\triangle F$  NOTCH MAY BE SQUARE

G. DRAWING FILENAME: MKT-TO247A03\_REV03

**Figure 23. TO-247, MOLDED, 3 LEAD, JEDEC VARIATION AB (Active)**

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Dimensions in Millimeters





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Fairchild®	MicroPak2™	SuperSOT™-3	UHC®
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FACT Quiet Series™	MotionMax™	SuperSOT™-8	UniFET™
FACT®	mWSaver®	SupreMOS®	VCX™
FAST®	OptoHiT™	SyncFET™	VisualMax™
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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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Rev. I66

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